

In re Patent Application of:

HARRIS ET AL

Serial No. **10/747,833**

Filed: **DECEMBER 29, 2003**

IN THE SPECIFICATION:

Please replace paragraph [0022] bridging pages 9 and 10, with the following rewritten paragraph:

[0022] The controller 103 has a CLOCK pin providing a CLOCK signal to a CLOCK input pin of each of the regulators 105. The controller 103 includes a pin FS coupled to one end of a resistor RFS, having its other end coupled to signal ground GND. As described further below, the frequency of the CLOCK signal is inversely proportional to the resistance of the resistor RFS. The controller 103 has a ready pin RDY coupled to an enable (EN) input pin of each of the regulators 103. The controller 103 includes an offset (OFST) pin ~~coupled to~~ coupled to one end of an offset resistor ROFS, which has its other end coupled to GND. A series-coupled resistor-capacitor (RC) circuit has one end coupled to the COMP pin and another end coupled to a feedback (FB) pin and to a VDIFF pin of the controller 103. The controller 103 includes a soft-start (SS) pin coupled to one end of a soft-start capacitor CSS, which has its other end coupled to GND. The controller 103 also includes an enable pin EN for externally enabling the functions of the controller 103.